

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,190,360 B1
APPLICATION NO. : 09/382677
DATED : March 13, 2007
INVENTOR(S) : Masaaki Hiroki

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 3, between lines 5 and 6, insert --frequency modulating a reference clock signal and obtaining a modulated clock signal;--.

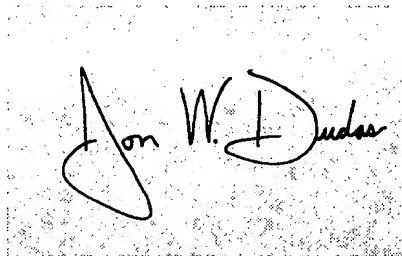
Column 3, line 7, replace "a reference clock signal" with --the modulated clock signal--.

Column 3, delete lines 18-19.

Column 3, line 21, replace "the modulated clock signal" with --a reference clock signal--.

Signed and Sealed this

Twentieth Day of November, 2007

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area with a light gray grid pattern.

JON W. DUDAS
Director of the United States Patent and Trademark Office